



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

WATANABE, Kenichi

Serial No.: 09/987,012

Filed: November 13, 2001

Group Art Unit: 2815

Examiner: Chris C. Chu

P.T.O. Confirmation No.: 6363

For: SEMICONDUCTOR WAFER DEVICE HAVING SEPARATED CONDUCTIVE
PATTERNS IN PERIPHERAL AREA AND ITS MANUFACTURING METHOD

PETITION FOR EXTENSION OF TIME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: August 6, 2004

Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the
Office Action dated March 15, 2004, for two months from June 15, 2004 to August 15, 2004.

Attached please find a check in the amount of \$420.00, to cover the cost of the extension
for a large entity. In the event that any additional fees are due in connection with this paper, please
charge our Deposit Account No. 01-2340.

Respectfully submitted,

ARMSTRONG, KRATZ, QUINTOS, HANSON & BROOKS, LLP

Susanne M. Hopkins
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PATENT TRADEMARK OFFICE